DETAILED ACTION

1.   This Office action is responsive to the preliminary amendment filed 06/08/2006. Claims

1-21 are presented for examination. Initialed and signed copies (2) of PTO-forms 1449 are

attached to this Office action.   If any copies are missing please notify the examiner in response

to this action.

          Priority has been claimed and the certified documents have been received.

2.   The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form

the basis for the rejections under this section made in this Office action:

          A person shall be entitled to a patent unless --

          (b) the invention was patented or described in a printed publication in this or a foreign
          country or in public use or on sale in this country, more than one year prior to the date of
          application for patent in the United States.

          Applicant cannot rely upon the foreign priority papers to overcome this rejection because

a certified translation of said papers has not been made of record. See M.P.E.P. 201.15.

3.   Claims 1-21 are rejected under 35 U.S.C. 102(b) as being anticipated by Baentsch et al

(PGPub 2002/0093856) and Shaylor et al. (Proceedings of ACM Sigplan).

          Baentsch appears to teach all the limitations of independent claims 1 and 18 including the

main part of the invention/embodiment. For example, the claimed "Java Card Virtual Machine"

corresponds to the Java Card Virtual Machine discussed throughout Baentsch. The claimed

"nonvolatile system memory" corresponds to 24k ROM discussed in section [0009]. The

claimed "nonvolatile application memory" corresponds 16K of NVRAM (flash). The claimed

"volatile working memory" corresponds to 1K of RAM. The claimed "variables memory

area…reserved in the volatile working memory" corresponds to any area in RAM that stores

variables or parameters. Mutable or static variables are generally stored in RAM

Shaylor teaches a Java Card Virtual Machine similar to Baentsch, however instead

teaches a larger 8K RAM, 32K NVRAM and 160K ROM Shayler specifically discussed the

storage or mutable or static variables in RAM in section 3.7.3.

The examiner believes all dependent claim features not specifically discussed above are

expressly or inherently taught by Baentsch or Shaylor. The remaining dependent claim features,

while part of the invention, do not appear essential to the main invention/embodiment found in

the independent claims. In the event applicant disagrees with the characterization of certain

dependent claim limitations as being "expressly or inherently" taught by the reference, applicant

may specify which claim limitations define over the **prior art of record**.

4.   The prior art made of record and not relied upon is considered pertinent to applicant's

disclosure. Applicant should review the prior art not relied upon for its relevance to the instant

claims. For example, much of the relevant art teaches the storage of variables or parameters in a

Java Card environment.

Any response to this action should be mailed to:

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

or faxed to:
> (571) 273-8300, (for Official communications intended for entry)

Or:
> (571) 273-4208, (for Non-Official or draft communications, please label "Non-Official" or "DRAFT")

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jack Lane whose telephone number is 571 272-4208. The examiner can normally be reached on Mon-Fri from 7:30AM-6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sanjiv Shah can be reached on 571 272-4098.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 571 272-2100.

/Jack A Lane/
Primary Examiner, Art Unit 2185